

FIGURE 1

DRAWING FOR PATENT APPLICATION
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USPTO CUSTOMER NO. 38845
ATTORNEY DOCKET NO: 08211/0200375-USO
INVENTOR: Ha Chu Vu
TITLE: GENERATING ADJUSTABLE-DELAY CLOCK
SIGNAL FOR PROCESSING COLOR SIGNALS
2/14

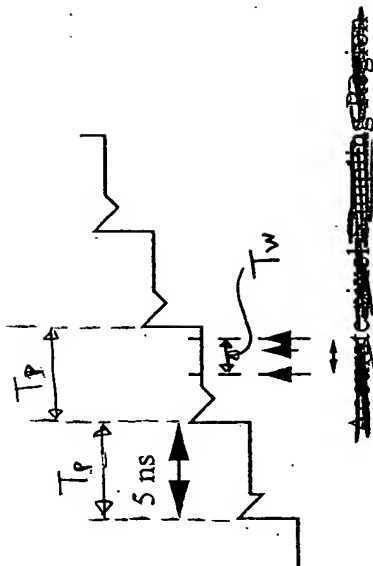


FIGURE 2

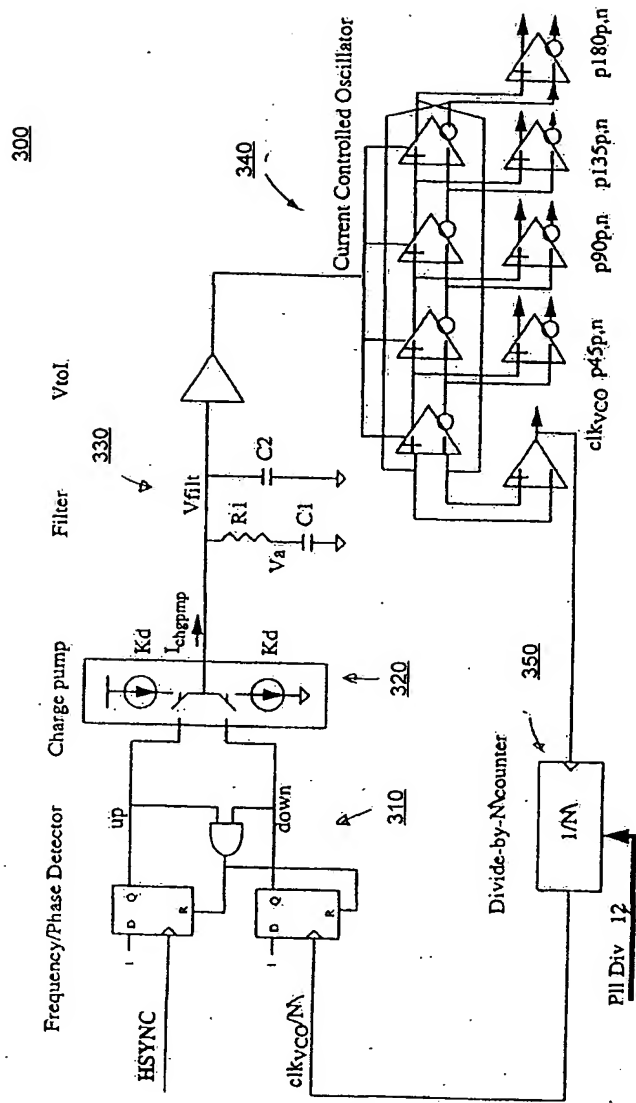


FIGURE 3

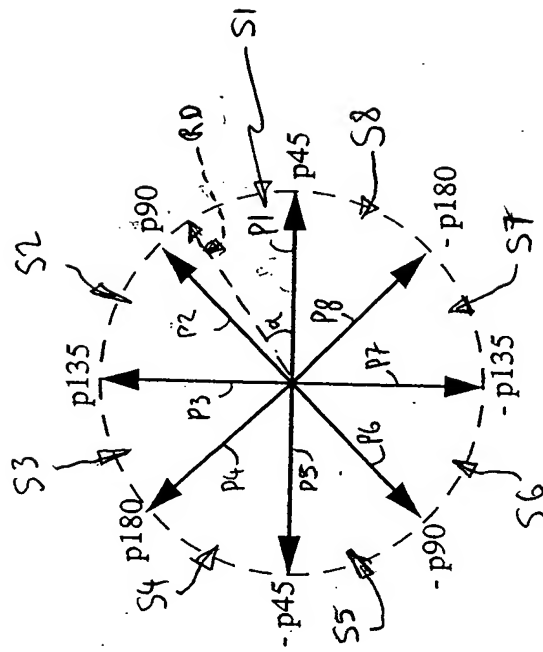


FIGURE 4

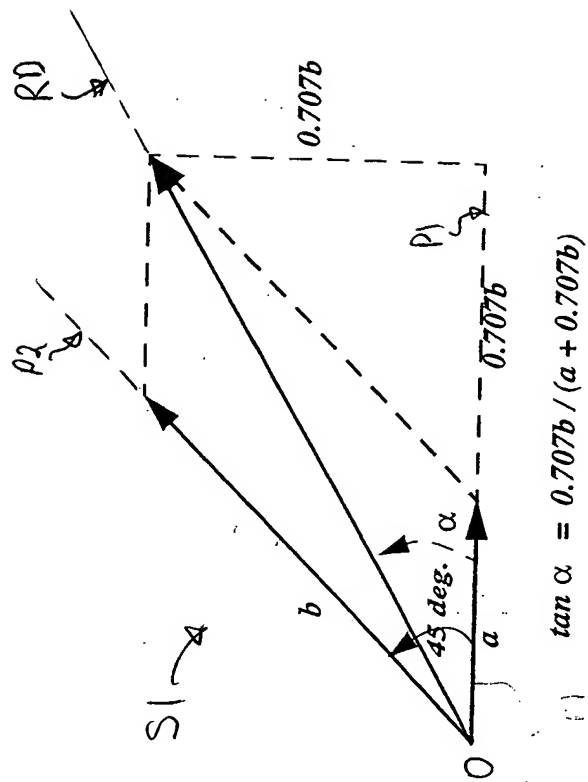


FIGURE 5

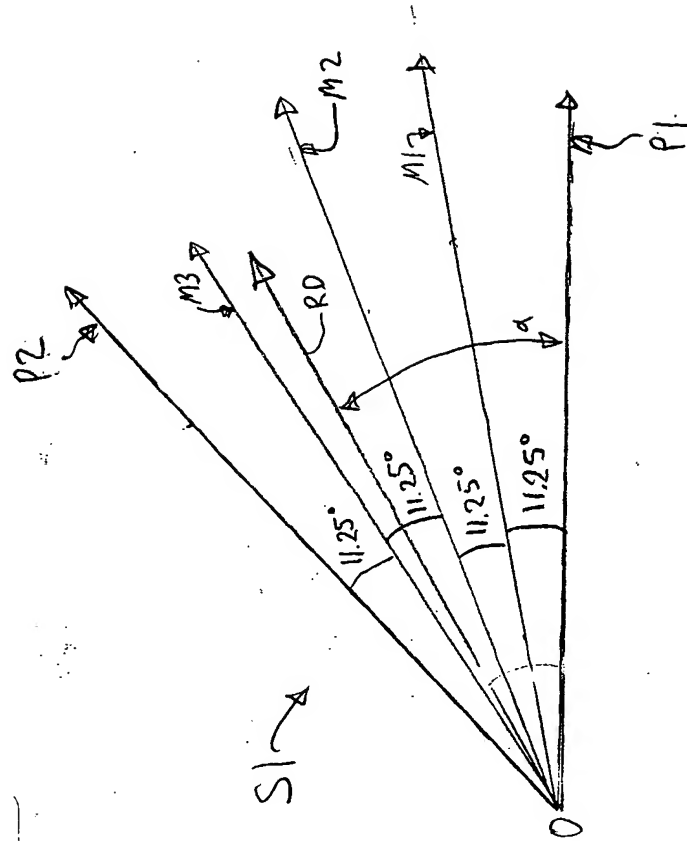


FIGURE 6

700

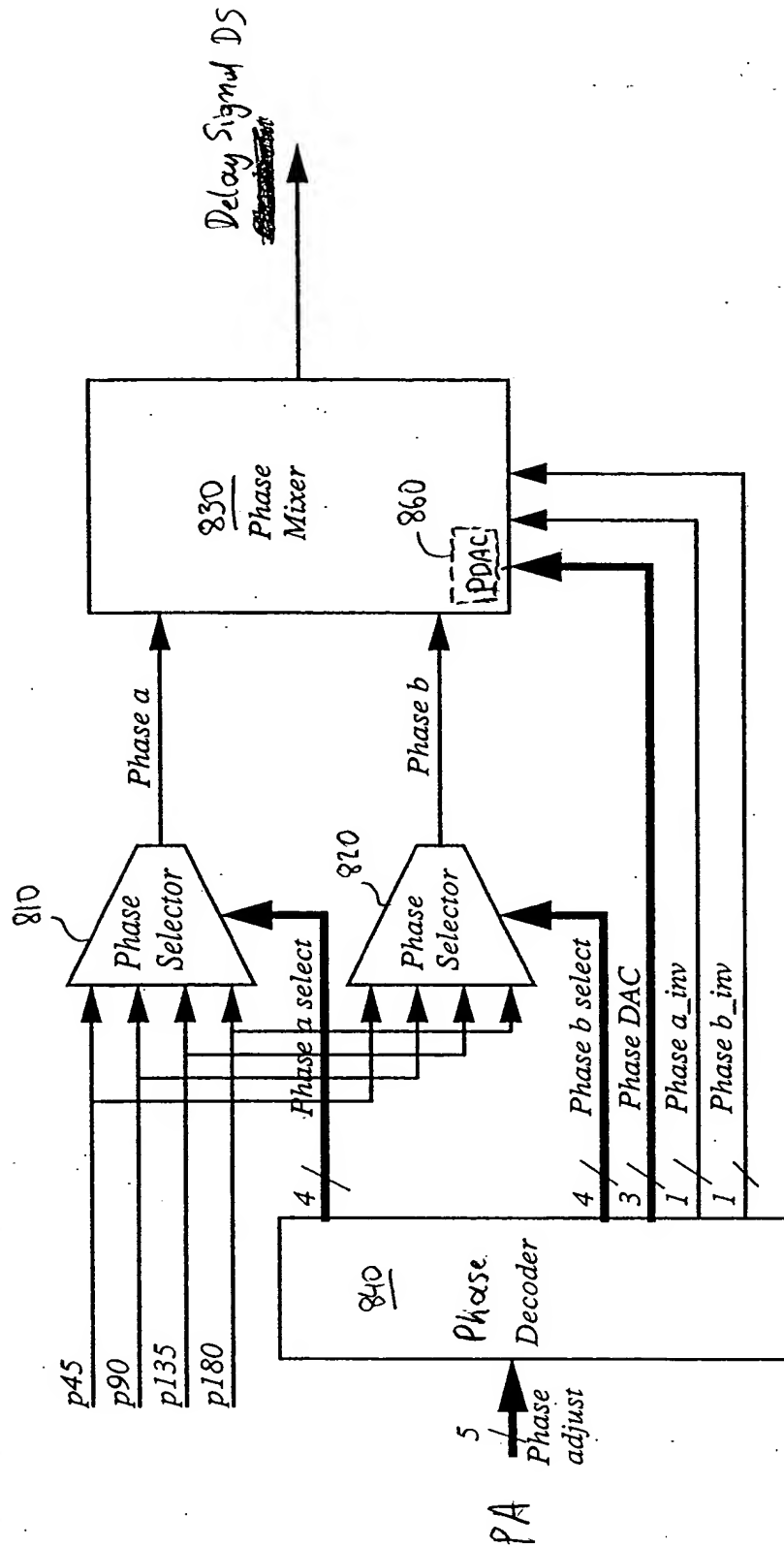
PHASE	SIMULATED SET 1		SET 2	
	α deg.	a	b	
P1	0	16	0	4 0
M1	11.25	12	4	3 1
M2	22.5	8	8	2 2
M3	33.75	4	12	1 3
P2	45	0	16	0 4

$\text{TOTAL 1} = 16$
 $\text{TOTAL 2} = 4$
Phase Step = $360 \text{ deg} / 32 = 11.25 \text{ deg}$
 $45 \text{ deg} / 4 = 11.25 \text{ deg}$

WEIGHT VALUES FOR
SIMULATED PHASES

FIGURE 7

800



PHASE ADJUSTER

FIGURE 8

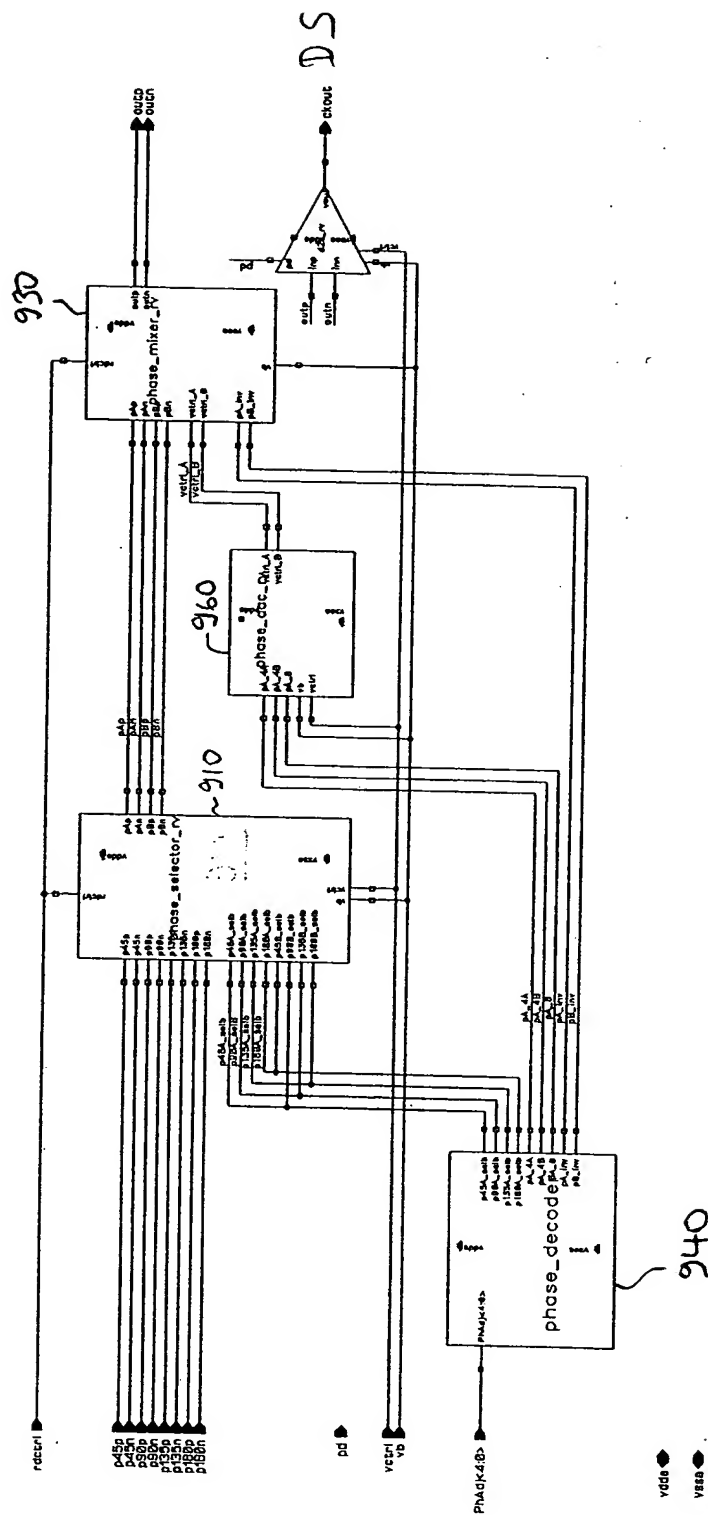


FIGURE 9

PHASE ADJUSTER (IC)

240

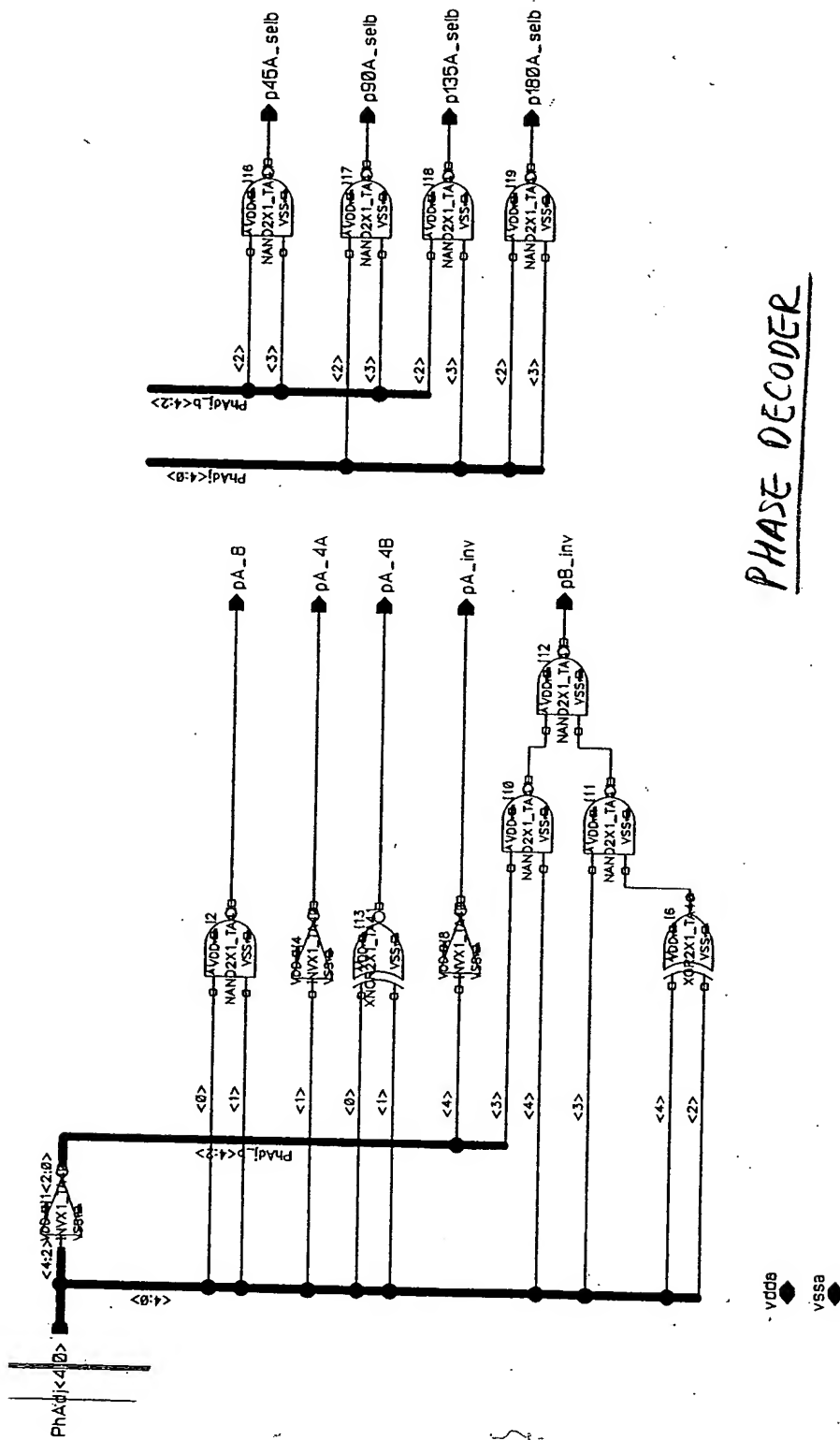


FIGURE 10

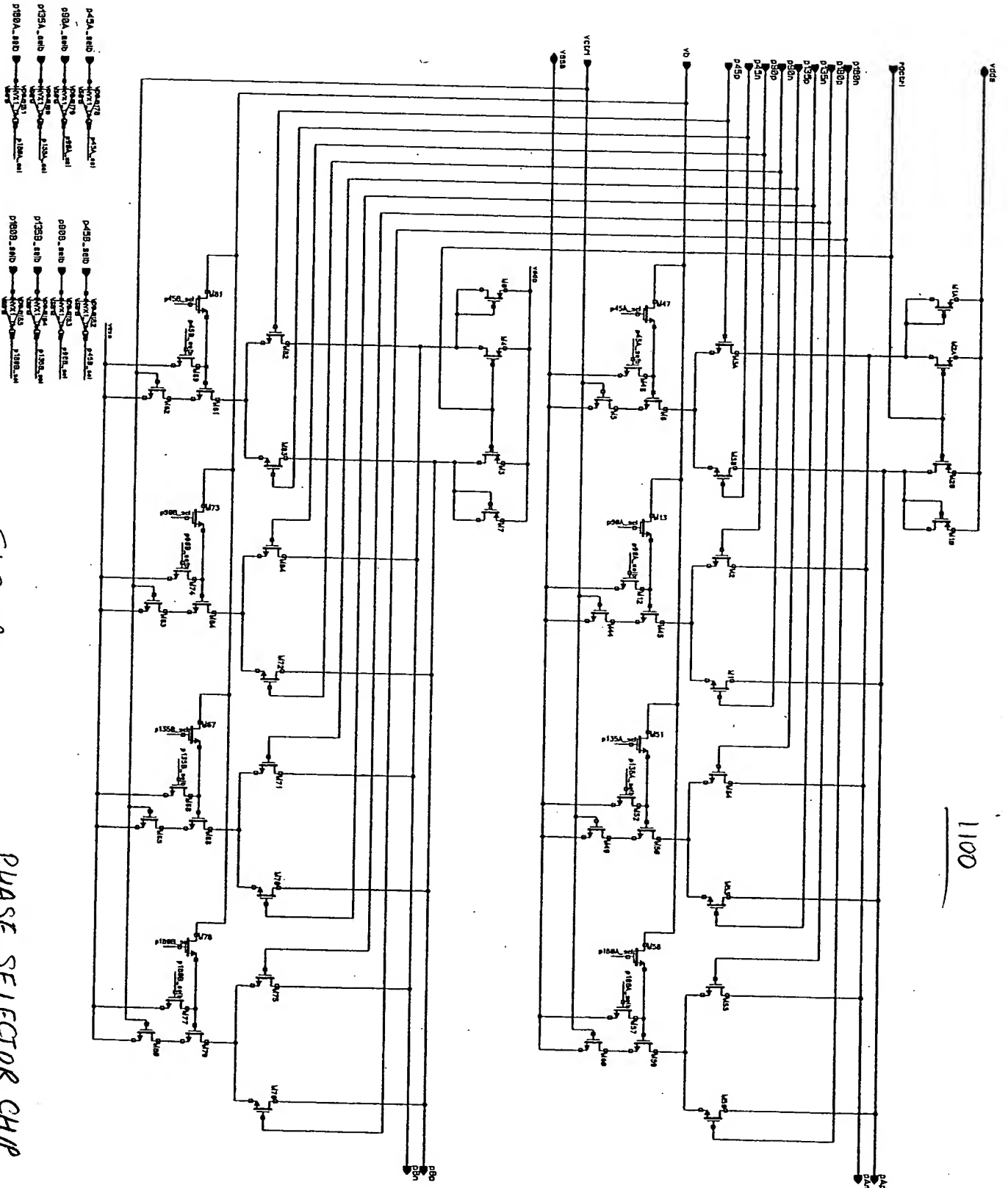
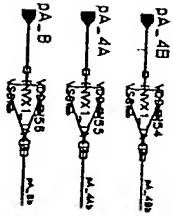
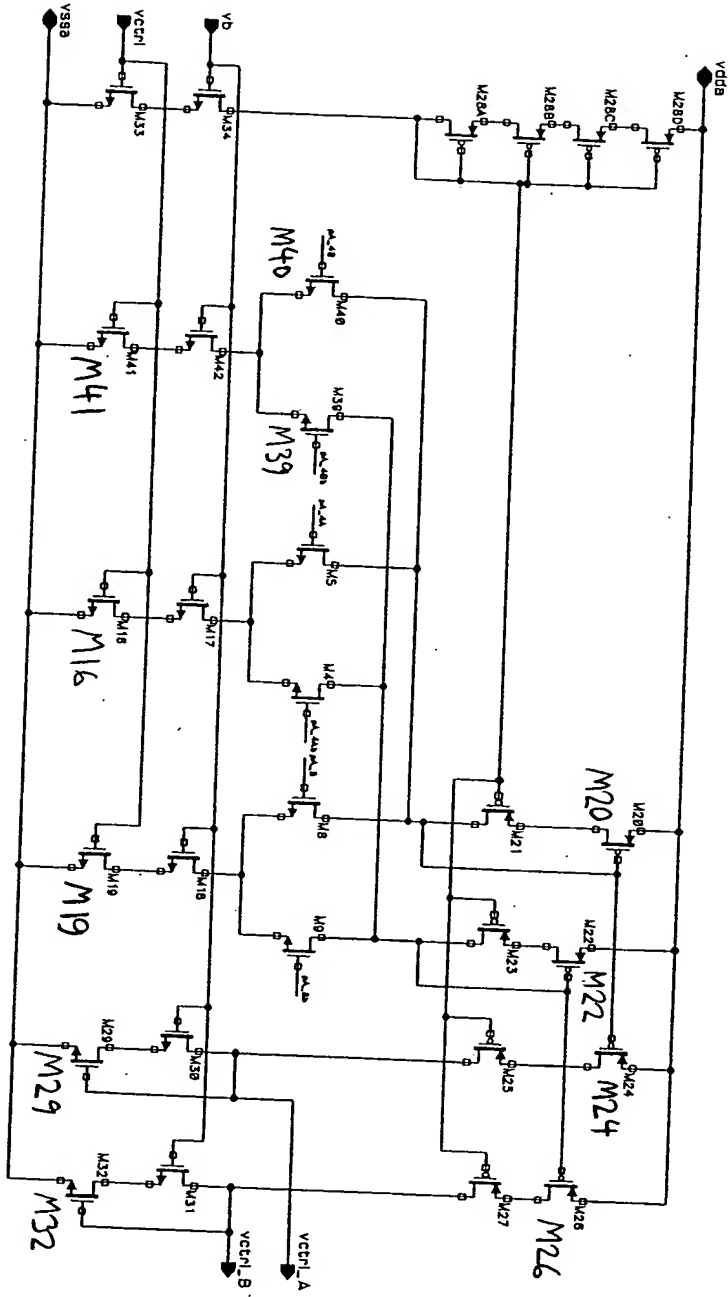


FIGURE 11
 PHASE SELECTOR CHIP

1200



PDAC

FIGURE 12

1300

DS

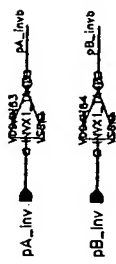
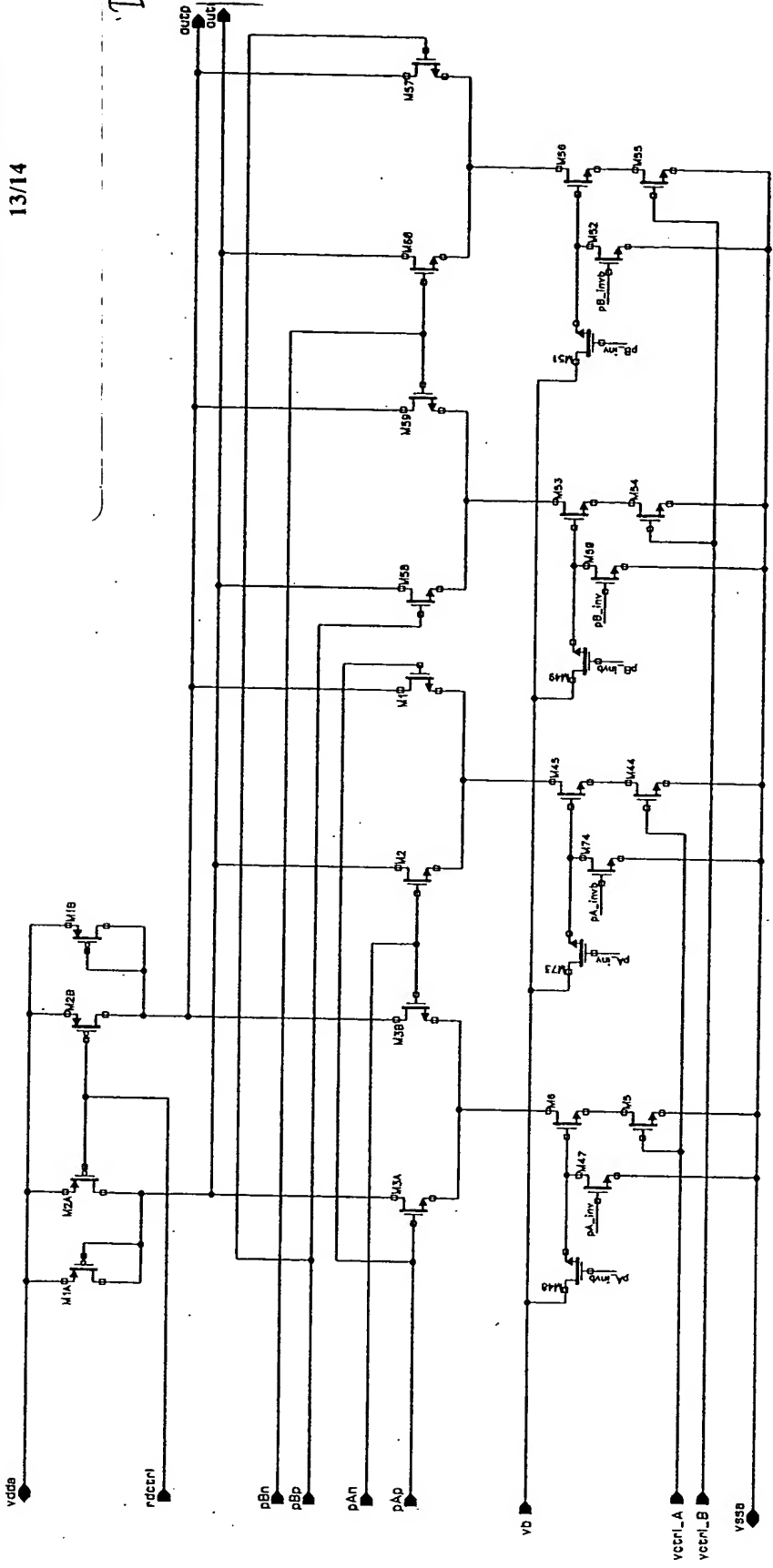


FIGURE 13

1400

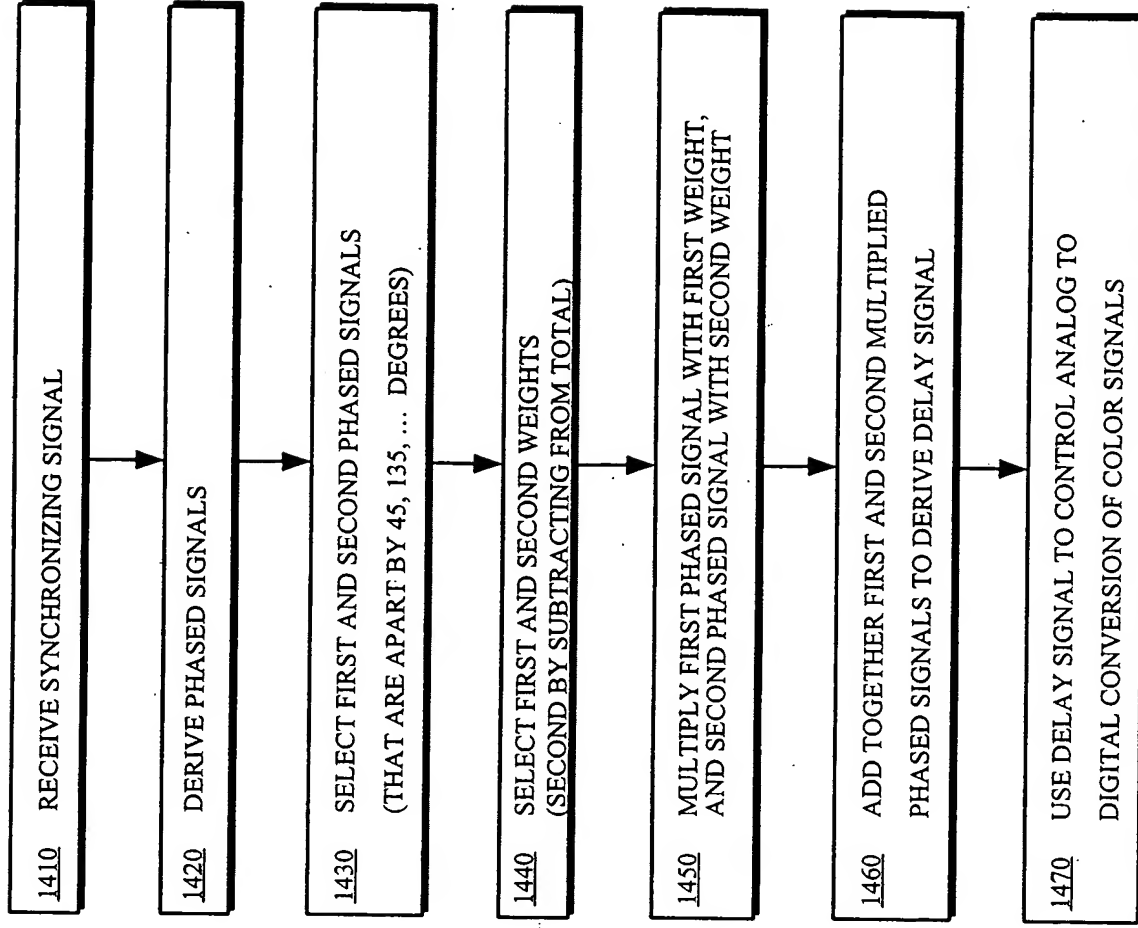


FIGURE 14